



Atty. Dkt. No. 016891-0858

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshitaka UEDA
Title: SEMICONDUCTOR DEVICE
Appl. No.: 10/627,608
Filing Date: 7/28/2003
Examiner: Nathan W. Ha
Art Unit: 2814
Confirmation Number: 4281

AMENDMENT UNDER 37 CFR 1.312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant acknowledges receipt of an Examiner's Amendment in the above-captioned application on March 15, 2007. Prior to payment of the issue fee, please amend the application as follows:

Amendments to the Specification begin on page 2 of this document.

Amendments to the Drawings begin on page 4 of this document, and include an attached replacement drawing sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 5 of this document.

Please amend the application as follows: